

107043680
01/10/02

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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APPL NUM 10043680	FILING DATE 01/10/2002	CLASS 365	SUBCLASS 222	GAU 2824	EXAMINER VAN THUONG UYEN
**APPLICANTS: Cowles Timothy; Shore Michael; Mullarkey Patrick;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/803,365 03/08/2001					
** FOREIGN APPLICATIONS VERIFIED: N/VTN					

US 6449203 issued 9/10/02
Y/VTN

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		500792.03
Verified and Acknowledged Examiners's initials VTN		
TITLE: Refresh controller and address remapping circuit and method for dual mode full/reduced density DRAMs		

11/4/03 Formal Drawings (4 sheets) set 11/10/02

DRAWINGS			CLAIMS ALLOWED	
Sheets Drwg. 4	Figs. Drwg. 5	Print Fig. 1	Total Claims 7	Print Claim for O.G. 1
<div>Drawing</div> <div>VAN THUONG UYEN PATENT EXAMINER</div> <div>ISSUE FEE IN FILE</div> <div>VanThu Nguyen 10/10/03 (Primary Examiner) (Date)</div> <div>(Assistant Examiner) (Date)</div> <div>(Legal Instruments Examiner) (Date)</div>			NOTICE OF ALLOWANCE MAILED	
			10-14-03	
			Amount Due 30	Date Paid 10/10/03
			ISSUE BATCH NUMBER	

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Fig.
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